

cycle: 0 1 2 3 4

clk.0

clk.0

clk.1

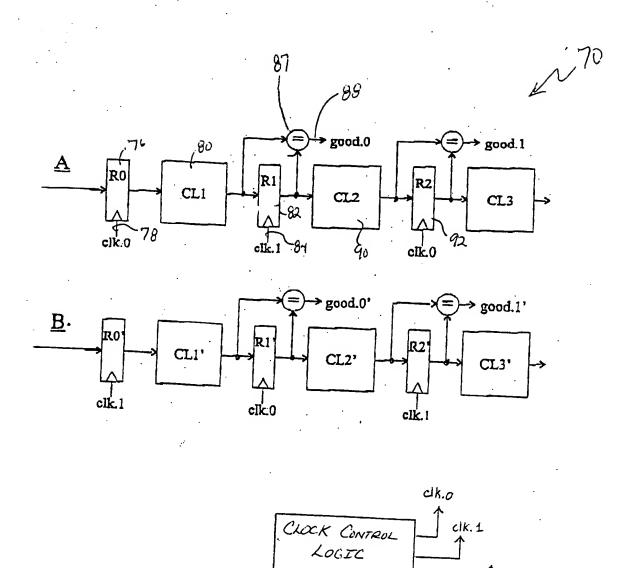
NS 0 1 2 3 4 (3) 5 (4)

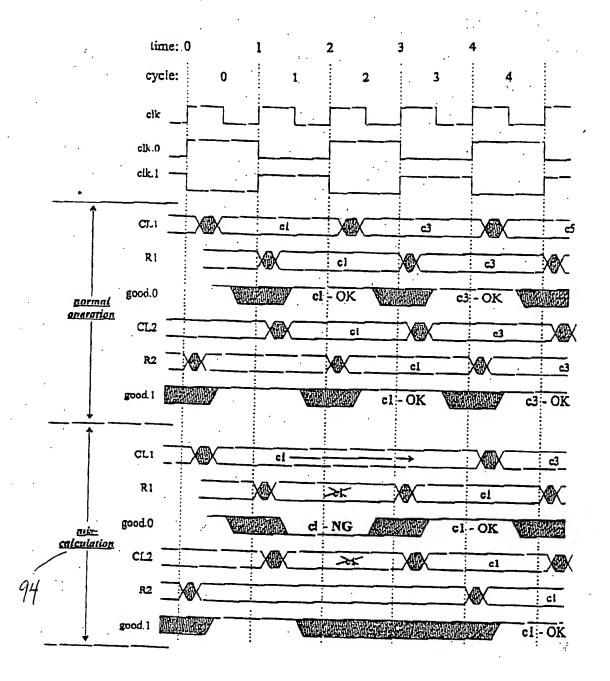
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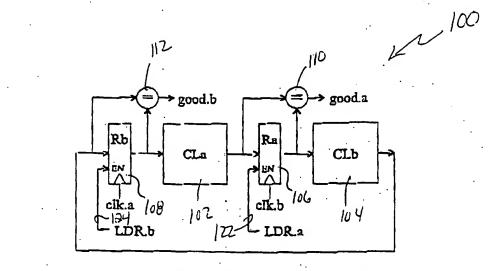
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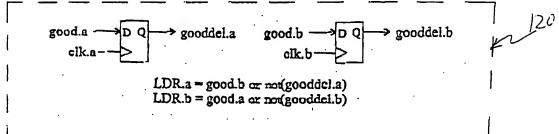
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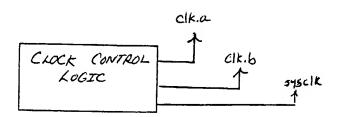
Q<sub>sys</sub> 0 1 2 3 4 (3)

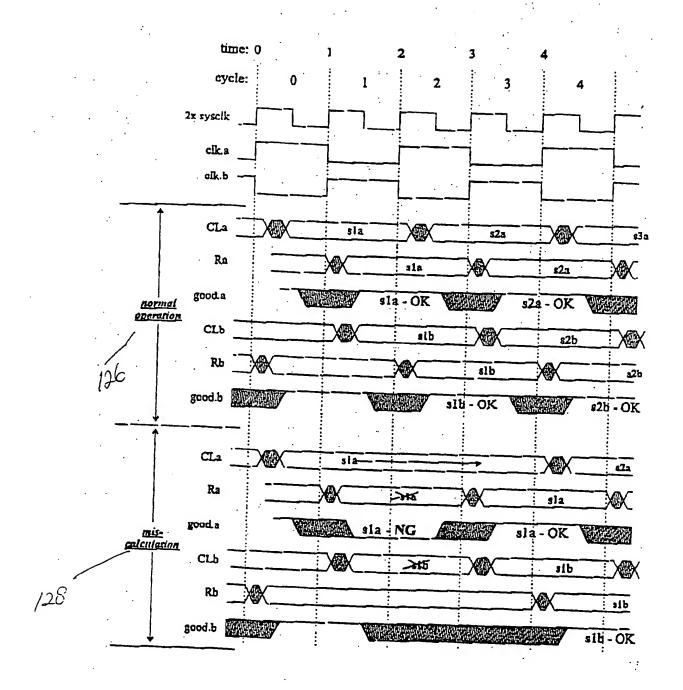


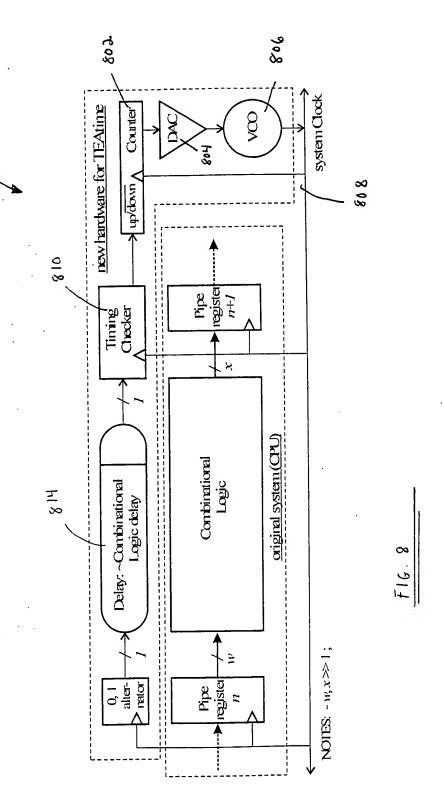


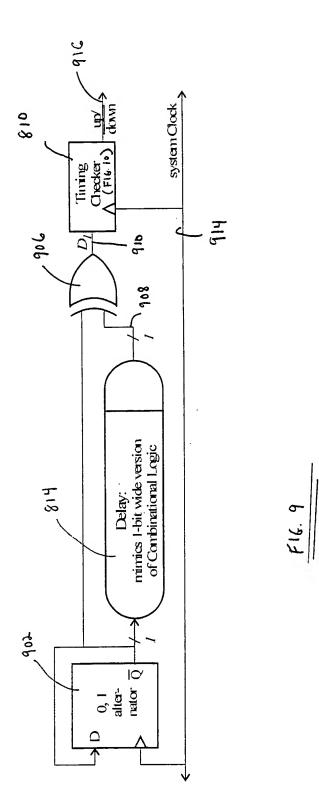


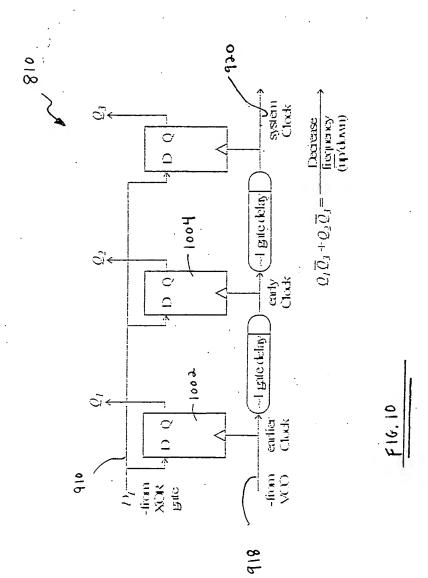


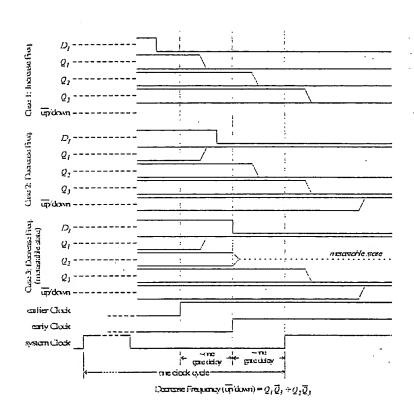












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